

filtered into an extraction thimble and then washed by Soxhlet extraction with methanol, hexane, and chloroform. The polymer was isolated from the chloroform extracts. End-group composition was evaluated with MALDI-TOF MS (Voyager-DE STR BioSpectrometry workstation by Biosystems, terthiophene matrix, linear mode, sometimes performed before extractions), and the polymer structure was confirmed with  $^1\text{H}$  NMR (Bruker 500 MHz instrument).

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## Hierarchical Structure: Silicon Nanowires Standing on Silica Microwires\*\*

By Changhui Ye,\* Lide Zhang, Xiaosheng Fang, Yinhai Wang, Peng Yan, and Jianwei Zhao

Silicon nanowires are attractive building blocks for the next generation of nanoelectronic devices because they can function both as the core of the devices and as the interconnectors between components.<sup>[1]</sup> With the ongoing miniaturization of silicon-based electronics, conventional lithography is reaching its limits. Fortunately, the self-assembly of nanoscale building blocks has proven successful in microelectronics and this technique is accessible to many laboratories, since costly fabrication lines are avoided.<sup>[2]</sup> However, the fabrication of nanodevices by either a self-assembly or improved Langmuir–Blodgett approach still entails lengthy and elaborate processing, although the new, emerging, so-called hierarchical struc-

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tures may represent an alternative toward the scaling up of the building blocks into functional electronic devices. Ren's group has systematically explored the growth of hierarchical oxide structures, the core and branches of which display certain orientation relationships.<sup>[3]</sup> In contrast to the well-known dendritic structures, the core and the branches of the hierarchical structures can be built from different materials. Ren, Wang, and Zhu and their respective co-workers have demonstrated that various oxide semiconductors, such as ZnO, In<sub>2</sub>O<sub>3</sub>, or SiO<sub>x</sub> hierarchical nanostructures, can be fabricated by simple evaporation and condensation processes, and they proposed that these novel structures could be applied in field emission, photovoltaics, supercapacitors, and so on.<sup>[4]</sup> However, Si is usually the core material for microelectronic devices, therefore hierarchical structures built directly from Si-based materials are expected to be an ideal research target towards the three-dimensional scaling-up of the building blocks as the basis for functional nanodevices.

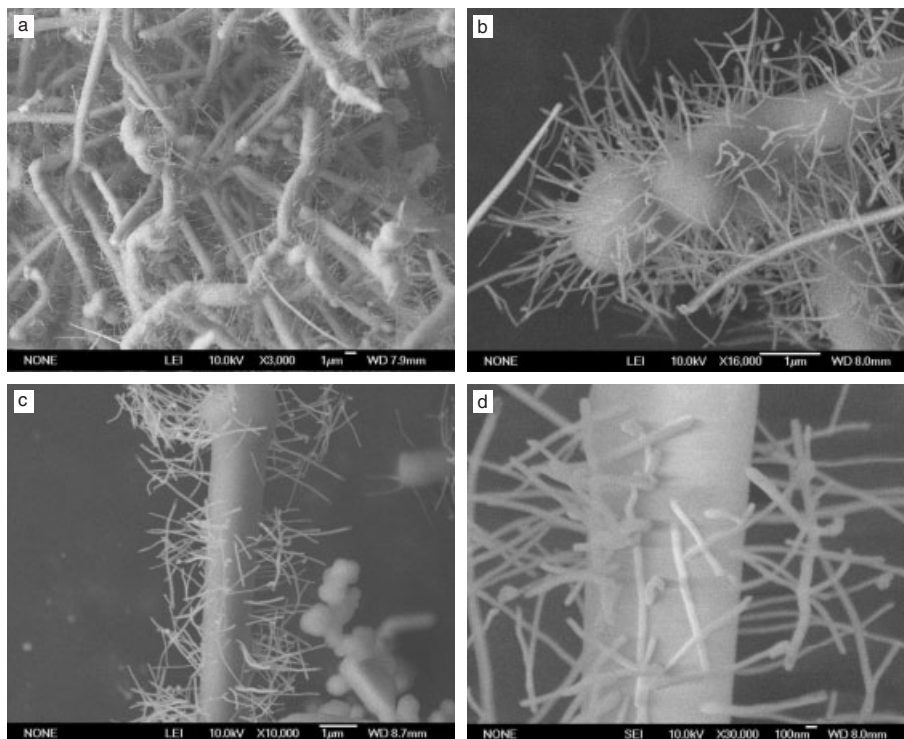
In this communication, we report the fabrication of a novel hierarchical structure, namely single-crystalline Si nanowires standing on silica microwires. We adopt the disproportionation reaction of SiO ( $2\text{SiO} \rightarrow \text{SiO}_2 + \text{Si}$ ) and are able to separate the deposition of SiO<sub>2</sub> and Si species into two steps to grow the hierarchical structures with metallic tin as a liquid-forming agent. This approach differs from the well-developed synthetic techniques for preparing Si or SiO<sub>2</sub> nanowires.<sup>[5]</sup>

Scanning electron microscopy (SEM) was employed to characterize the morphology of the as-synthesized product. The general morphologies of the hierarchical structures are displayed in Figure 1. A large quantity of microwires with numerous nanowires standing on them are shown in Figure 1a. A close-up view of the wires (Fig. 1b) reveals that many nanowires terminate with a spherical particle, which is typical of a vapor-liquid-solid (VLS) growth mechanism.<sup>[6]</sup> Figure 1c shows a straight microwire with many nanowires growing from it. The enlarged section of this image shown in Figure 1d indicates that these nanowires are grown-in from the microwire. The nanowires are straight and smooth with a typical length of several micrometers and a diameter ranging from 40 to 70 nm.

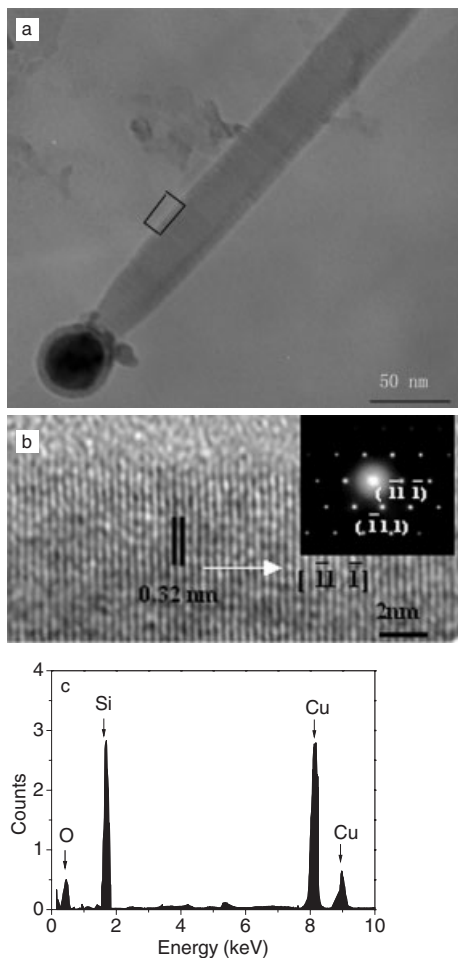
Transmission electron microscopy (TEM) observations allowed us to gain insight into further details of this structure. The nanowires could be broken off from the microwire by

ultrasonic treatment in acetone solution. The low-magnification TEM image of a typical nanowire (Fig. 2a) reveals that the nanowire has a smooth surface with a particle at its tip. The high-resolution electron microscopy (HRTEM) image of this nanowire (Fig. 2b) shows the lattice fringes perpendicular to the wire axis. The spacing between the neighboring fringes is equal to 0.32 nm, which corresponds to the separation of the {111} lattice planes of Si. There is a thin amorphous layer on the nanowire, which may be SiO<sub>x</sub>, in agreement with previous reports.<sup>[5]</sup> The electron diffraction (ED) pattern (inset in Fig. 2b) taken along the  $\langle 110 \rangle$  zone axis agrees well with the HRTEM image and further indicates that the nanowire is a single crystal. The two spots in the ED pattern indexed as  $(\bar{1}\bar{1}\bar{1})$  and  $(\bar{1}11)$  indicate that these two lattice planes possess an inclination of 70.5°, which agrees with crystallography data of the cubic lattice of Si. This means that the nanowire grows along the  $[\bar{1}\bar{1}\bar{1}]$  direction. Energy-dispersive X-ray spectroscopy (EDX) reveals that this nanowire is composed of Si (Fig. 2c). The trace amount of oxygen observed may come from the amorphous SiO<sub>x</sub> layer; Cu comes from the specimen holder. The particle at the tip of the nanowire consists of Sn, Si, and O (not shown here) and assists the growth of the Si nanowire.

The growth of the hierarchical structures can be divided into two consecutive steps. Upon decomposition of the SiO

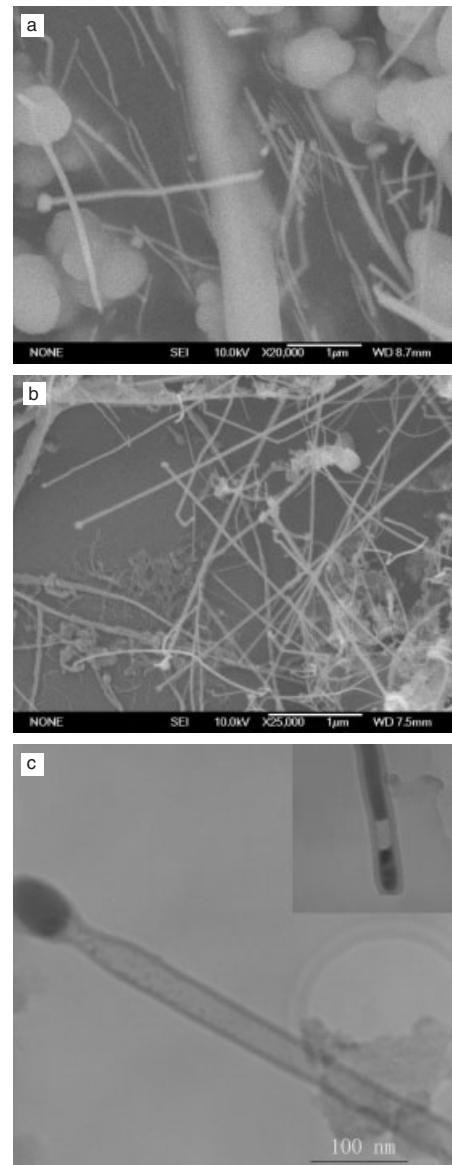


**Figure 1.** SEM images of the Si-based hierarchical structures. a) Low-magnification view of a large quantity of the hierarchical structures. b) High-magnification view of a typical hierarchical structure. c) A straight microwire with numerous nanowires erupting from it. d) Enlargement of the central section in (c).



**Figure 2.** a) TEM image of a Si nanowire with a spherical nanoparticle at its tip. b) HRTEM image of the boxed section in (a). Inset is the corresponding ED pattern taken along the  $\langle 110 \rangle$  zone axis. c) EDX pattern of this nanowire, where Si and a small amount of O are observed.

vapor to  $\text{SiO}_2$  and Si,  $\text{SiO}_2$  may grow rapidly as microwires or a nanoparticle aggregate. Si will form an alloy with Sn (formed by the decomposition of SnS nanoparticles) and will condense much slower than  $\text{SiO}_2$  does owing to the low eutectic temperature of Si–Sn alloy ( $232.0^\circ\text{C}$ ). Therefore, the deposition of the Si–Sn alloy on the surface of the  $\text{SiO}_2$  microwires and subsequent VLS growth of Si nanowires from saturated Si–Sn alloys gives birth to the hierarchical structures. However, the deposition of the Si–Sn alloy on  $\text{SiO}_2$  microwires is not necessarily uniform; consequently, some  $\text{SiO}_2$  microwires grow without Si nanowires on them. Moreover, deposition of the Si–Sn alloy on the substrate may directly induce the Sn-assisted growth of Si nanowires, and this has indeed been observed in the SEM characterization. Figure 3a shows the growth of  $\text{SiO}_2$  microwires and Si nanowires in close proximity. EDX analysis reveals that the microwires are indeed composed of Si and O with an atomic ratio close to 1:2 and that the nanowires composed of Si and a small amount of O (not shown).



**Figure 3.** a) SEM image of the coexistence of  $\text{SiO}_2$  microwires and Si nanowires. b) SEM image of the nanowires deposited on the substrate with Sn as the liquid-forming agent in the control experiment. c) TEM image of the by-product in the source region after the reaction in the regular experiment for the preparation of the hierarchical structure. The typical products therein are  $\text{SiO}_2$  nanotubes and  $\text{SiO}_2$  nanotubes partially filled with Sn (inset).

The questions still to be answered are: what is the determining factor for the two-step hierarchical assembly of Si nanowires on  $\text{SiO}_2$  microwires; and why similar phenomena have not been reported by other researchers during the preparation of Si nanowires using the same precursor (SiO). We propose that the in situ alloying of Si and Sn to produce the liquid phase and transport to the substrate following the  $\text{SiO}_2$  phase are most probably responsible for the formation of this particular structure. In a control experiment with micrometer-sized metallic Sn deposited directly on the substrate and pure SiO

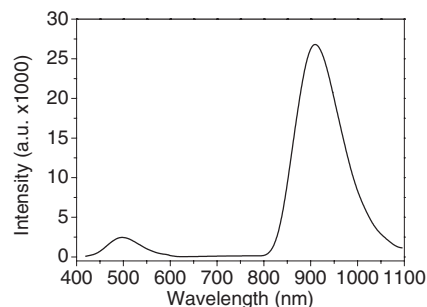
particles as the source material, only fine nanowires of Si and SiO<sub>2</sub> were produced, as displayed in Figure 3b. This may reveal the importance of the in situ alloying process and the separation of SiO<sub>2</sub> and Si species in the formation of the hierarchical structures. When trying to elucidate the operation of the two-step process, we analyzed the by-product left in the source region (originally containing SiO powder and SnS nanoparticles) in the regular experiment for the preparation of the hierarchical structure. Instead of the above-mentioned hierarchical structures, a distinct morphology was observed. The TEM image in Figure 3c represents a general structure of this morphology. It appears that Sn-assisted growth of silica nanotubes and silica nanotubes partially filled with Sn (inset in Fig. 3c) have been obtained, similar to the In-assisted silica and In-filled silica nanotubes reported recently by Li et al.<sup>[7]</sup> The above findings and the fact that simultaneous deposition of Si and SiO<sub>2</sub> usually gives SiO<sub>2</sub>-sheathed Si nanowires explicitly indicates that a two-step process is responsible for the formation of the hierarchical structure. This is in agreement with the observations made by other researchers in growing oxide hierarchical structures, where a two-step process has also been proposed.<sup>[3,4]</sup>

As for the second step in the present study, the multiple nucleation of silicon nanowires on silica microwires from the initially deposited Sn–Si alloy particles supersaturated with Si followed by the supply of Sn–Si alloy in the gas phase is fully consistent with a VLS growth mechanism and the basal-growth aspects of the Si nanowires seen in the experiments.<sup>[6]</sup>

It is noteworthy that metals with a low melting temperature such as Ga (29.8 °C), In (156.6 °C), and Sn (231.9 °C) generally form alloys with Si at a low eutectic temperature rather close to the melting point of the respective metals. Furthermore, the atomic content of Si in the alloys at the eutectic temperature are as low as, or even lower than, one part per thousand. In particular, the solubility of Si in the alloy with Sn at the substrate temperature (500 °C) is 0.4 at.-%. The effectiveness of these metals in directing the growth of a wide variety of Si-based structures, first suggested by Sunkara et al.,<sup>[8]</sup> and confirmed by Pan et al.,<sup>[9]</sup> Li et al.,<sup>[7]</sup> and Sun et al.,<sup>[10]</sup> further suggests the importance of the nanometer-sized Sn liquid-forming agent for the derivation of the Si-based hierarchical structure in the present study.

The room-temperature photoluminescence (PL) spectrum of the hierarchical structures was measured at an excitation wavelength of 325 nm (Fig. 4). The emission spectrum reveals that the as-synthesized Si-based structure has a strong near-IR emission band centered at 910 nm and a weaker green one at 500 nm. The green emission band can be attributed to radiative recombination from the defect centers in the over-coated silicon oxide layer, such as oxygen vacancies, as suggested by Liao et al.<sup>[11]</sup> The near-IR emission band may possibly originate from defective Si nanowires, as proposed by Teo et al.<sup>[12]</sup>

In summary, novel hierarchical structures of single-crystalline Si nanowires standing on SiO<sub>2</sub> microwires have been synthesized by adopting Sn as the liquid-forming agent. The derivation of this kind of structure involves a two-step pro-



**Figure 4.** PL spectrum of the hierarchical structures, where a strong peak centered at 910 nm and a weak peak centered at 500 nm are observed.

cess, namely growth of SiO<sub>2</sub> microwires followed by deposition of Si and Sn alloys, which direct the growth of the Si nanowires. The intense near-IR PL suggests possible applications of the hierarchical structures in nanoscale optoelectronic devices. This facile scaling-up of the nanoscale building blocks of Si nanowires has implications in the fabrication of functional nanodevices such as optoelectronics and gas sensors.

### Experimental

A horizontal ceramic tube (inner diameter 25 mm; length 80 cm) was mounted inside a tube furnace. SnS nanoparticles (0.5 g; average grain size ca. 60 nm) and analytical grade SiO powders (2 g, with diameter about 10 μm; purity greater than 99.99 %) were put in an alumina boat and then loaded into the central region of the ceramic tube. Then, a pre-cleaned Si wafer was placed at the downstream side of the source materials at a distance of 8 cm. After the tube had been purged with high-purity Ar for 30 min, the Ar flow-rate was set at 25 sccm. The temperature at the central region of the furnace was increased to 1200 °C in 10 min, and then maintained at this temperature for 2 h. The temperature at the substrate was close to 500 °C, as determined from the pre-measured temperature-gradient curve. After the furnace had cooled to room temperature, a dark gray wool-like product was deposited on the substrate. The collected products were characterized by scanning electron microscopy (SEM; JEOL JSM 6300), transmission electron microscopy (TEM; Hitachi 800 at 200 kV; HRTEM, JEOL 2010, at 200 kV), and energy-dispersive X-ray spectroscopy (EDX). The room-temperature photoluminescence (PL) spectrum was recorded with an Edinburgh luminescence spectrometer (FLS 920) using a xenon lamp (900) as the excitation source.

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## Micelle-Mediated Synthesis of Single-Crystalline Selenium Nanotubes\*\*

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Considerable attention has recently been directed towards one-dimensional (1D) nanostructures, such as wires, rods, belts, and tubes, owing to their unique physical and chemical properties and potential applications in nanoscale devices.<sup>[1]</sup> In particular, much effort has been devoted to the controlled synthesis of inorganic nanotubes since the discovery of carbon nanotubes.<sup>[2]</sup> A variety of inorganic nanotubes based on layered or pseudo-layered materials have been synthesized by

using high-temperature or hydrothermal processes; examples include boron nitride, metal dichalcogenides, metal dihalides, metal oxides and hydroxides, metal borates, as well as metals.<sup>[3]</sup> Alternatively, nanotubes made from silica, alumina, silicon, and metals that do not possess a layered crystal structure have been fabricated by employing various templates such as porous membranes, carbon nanotubes, inorganic nanowires/nanorods, and organized assemblies of organic molecules,<sup>[4]</sup> however, these nanotubes are usually either amorphous or polycrystalline. Notably, there are a few reports of single-crystalline nanotubes composed of semiconductors with non-layered structures. Single-crystalline GaN<sup>[5]</sup> and Si<sup>[6]</sup> nanotubes have been produced by epitaxial overgrowth against suitable nanowire templates using vapor deposition processes. Interestingly, single-crystalline tellurium nanotubes have been obtained via a solution-phase approach, i.e., the polyol process where ethylene glycol refluxed at 197 °C served as both solvent and reducing reagent.<sup>[7]</sup> This soft solution processing strategy is promising but it still remains to be extended to inorganic materials other than tellurium. Therefore, it is a great challenge to develop new soft synthetic strategies that produce single-crystalline nanotubes from non-layered materials.

As an important elemental semiconductor, selenium shows many interesting properties, such as a relatively low melting point (~490 K), a high photoconductivity ( $\sim 8 \times 10^4 \text{ S cm}^{-1}$ ), nonlinear optical responses, and a high reactivity toward a variety of chemicals, and it finds commercial applications in photovoltaic cells, rectifiers, mechanical sensors, photographic exposure meters, and xerography.<sup>[8]</sup> Since the availability of 1D selenium nanostructures is expected to bring in new types of applications or to enhance the performance of the currently existing devices as a result of quantum-size effects, a number of synthetic methods have been developed to fabricate selenium nanorods and nanowires. Monoclinic selenium (m-Se) nanowires with a polycrystalline structure were obtained by using the protein cytochrome *c*<sub>3</sub> to reduce selenate ( $\text{SeO}_4^{2-}$ ).<sup>[9]</sup> Xia et al. synthesized single-crystalline trigonal selenium (t-Se) nanowires via the reduction of selenious acid ( $\text{H}_2\text{SeO}_3$ ) with excess hydrazine by solution refluxing<sup>[10]</sup> or sonochemical approaches.<sup>[11]</sup> Subsequently, t-Se nanowires and nanorods were produced by the dismutation of  $\text{Na}_2\text{SeO}_3$  under acidic condition<sup>[12]</sup> or the solution-mediated transformation from Se powders.<sup>[13]</sup> In addition, laser ablation and an evaporation and condensation process were used to prepare t-Se nanorods and nanowire networks, respectively.<sup>[14]</sup> It is noteworthy that Xia et al. have successfully synthesized a variety of novel 1D nanostructures including  $\text{Ag}_2\text{Se}$  nanowires, CdSe nanotubes, and Pt nanotubes by templating against the obtained Se nanowires.<sup>[15]</sup> However, to the best of our knowledge, there has been no report on the synthesis of uniform selenium nanotubes although large tubular single crystals of selenium were obtained under solvothermal conditions<sup>[16a]</sup> and irregular or broken selenium nanotubes were produced through a hydrothermal process followed by sonication.<sup>[16b]</sup> Here we describe a unique, facile, large-scale synthesis of single-crystalline t-Se nanotubes in micellar solutions of a

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